

Application No. 09/252,551

3 high gain error generator, a loop filter, and an oscillator
4 circuit, the high gain error generator responsive to characteristic
5 values of the timing signals, and wherein the second timing loop
6 includes a low gain error generator, a loop filter, and an
7 oscillator circuit, the high gain error generator responsive to
8 characteristic values of the data signals.

REMARKS

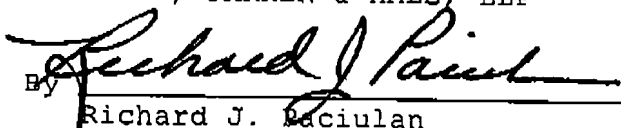
In the above-referenced Examiner Interview, Examiner Lee indicated that an amendment received by the U.S.P.T.O. on March 20, 2000 did not conform to reissue application amendment format, namely that the entire claim amendment had not been fully underlined. Accordingly, enclosed herein are claim amendments to Claims 134 and 142 - 144 in reissue amendment format. The amended claims merely change the dependencies as submitted in the above-referenced objected amendment. No further amendments to the claims have been made.

Accordingly, in view of the above amendment and remarks it is submitted that the allowable claims in the reissue application are now in condition for allowance and that therefore entry of the amendment and the issuance of a Notice of Allowance is hereby requested.

Respectfully submitted,

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